

D

D

C

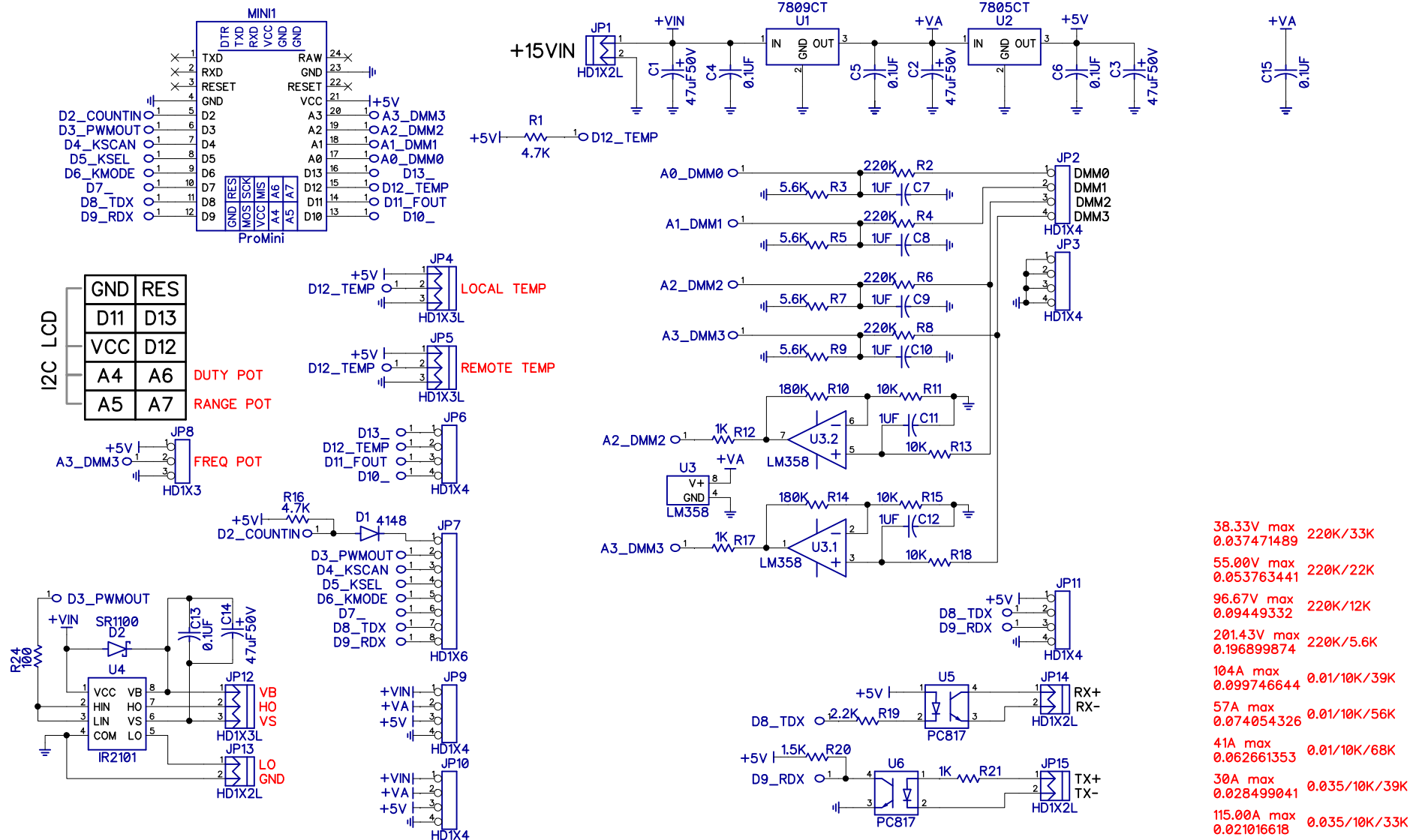
C

B

B

A

A



DMM0	DMM1	DMM2	DMM3	TEMP	COUNTER	PWM	UART
201V	201V	201V	201V	YES	YES	YES	YES

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C. E. S. WEST COVINA, CALIFORNIA, U.S.A.

CPU SCHEMATICS

SIZE	DWG NO.	REV
A	CES180501_V01.dch	01
CES180501_V01.dch		Sheet 1 of x