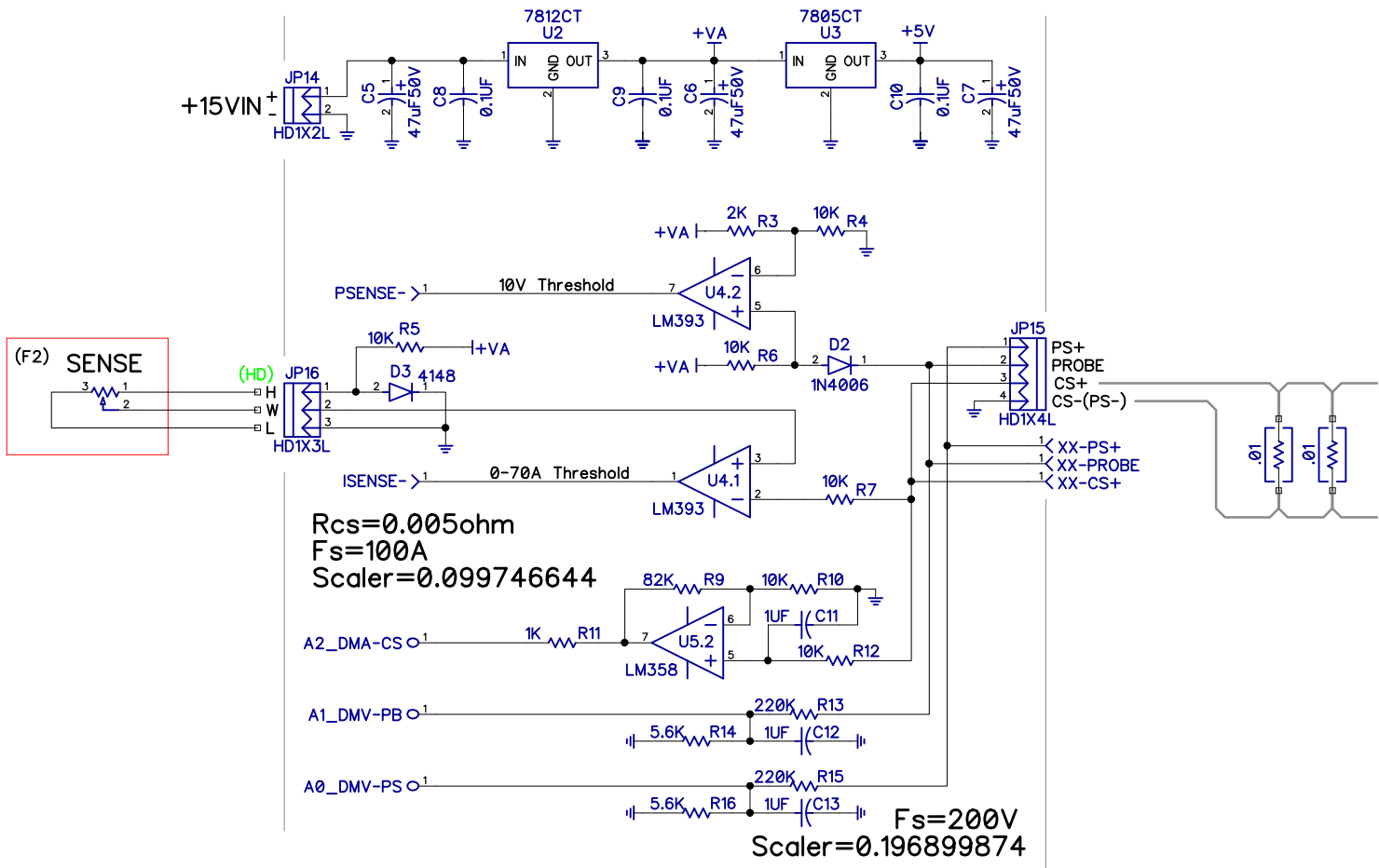
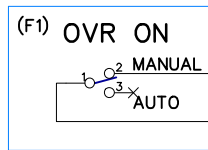
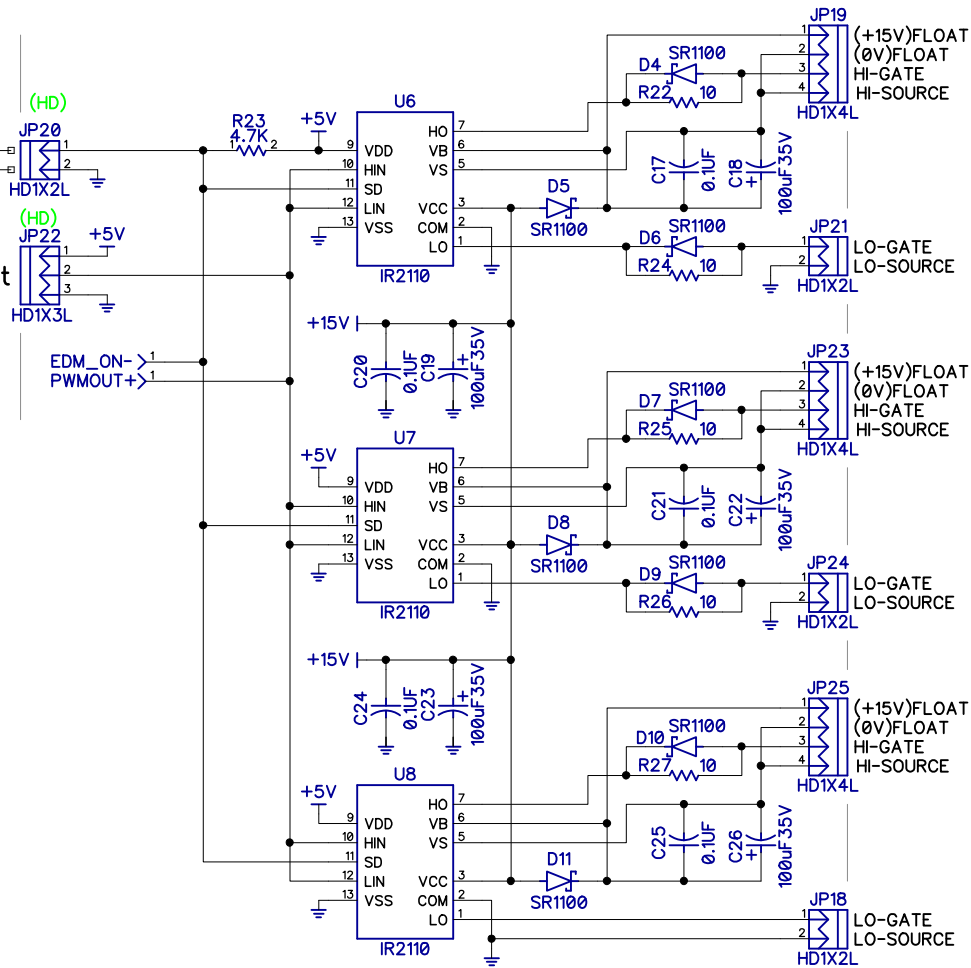


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	CPU SCHEMATICS		
	SIZE	DWG NO.	REV
	A	CES180325-3 CES180325-3_v01.dch	01 Sheet 2 of 5



Signal_Output



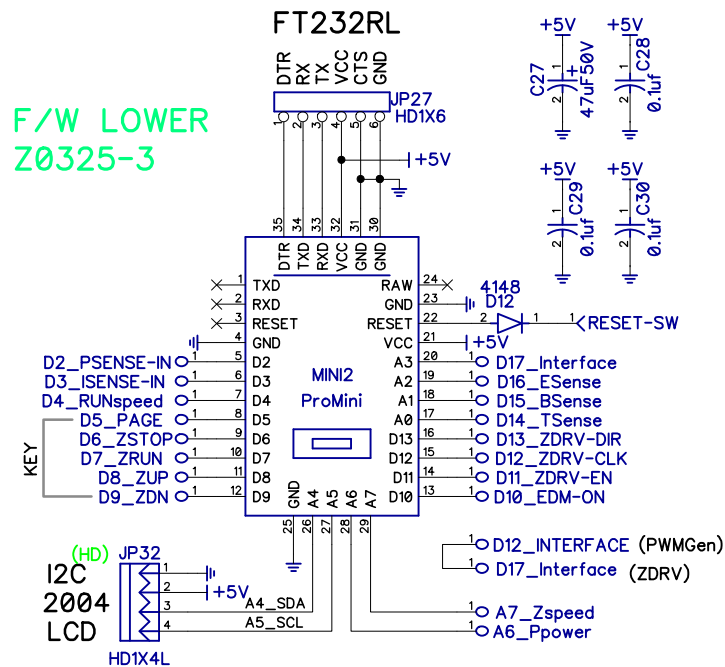
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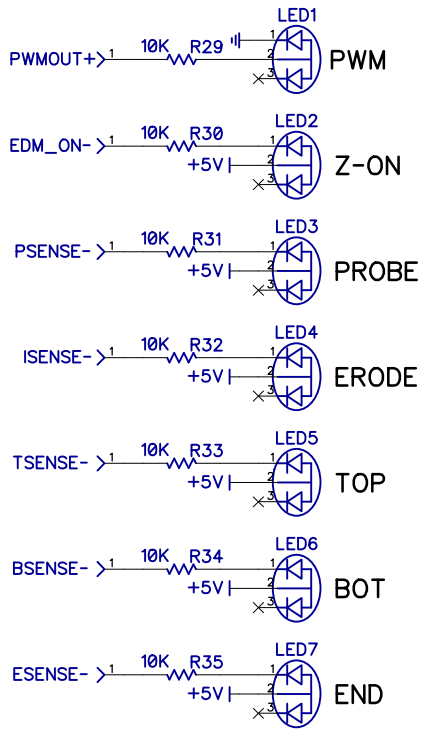
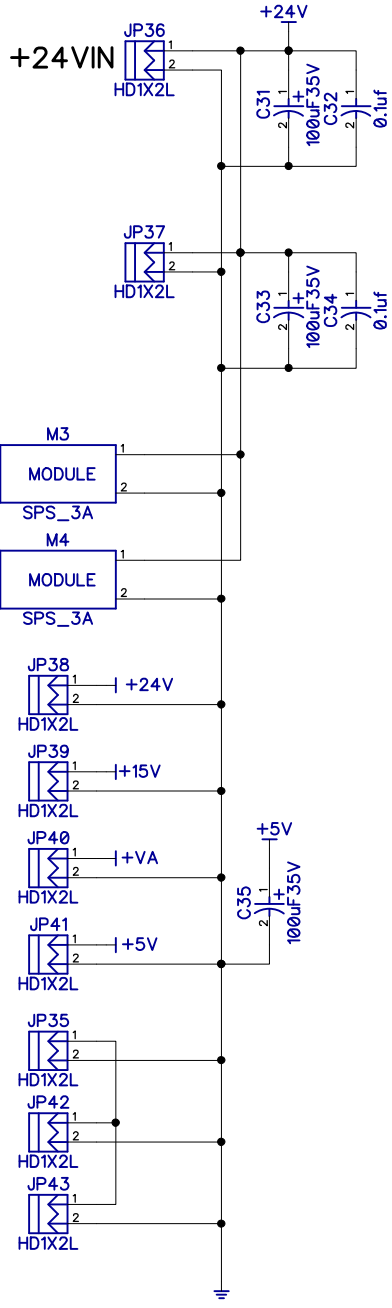
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		SIZE A	REV 01
		DWG NO. CES180325-3	Sheet 5 of 5