

D

C

B

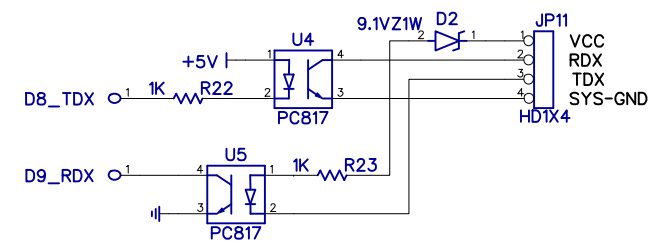
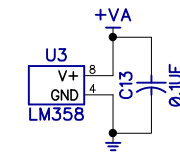
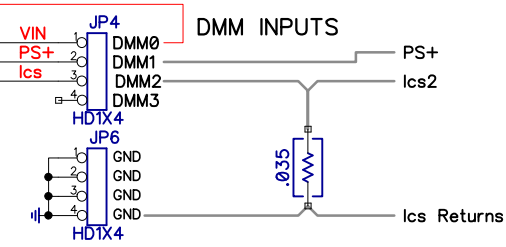
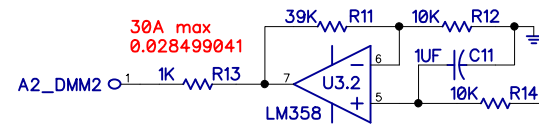
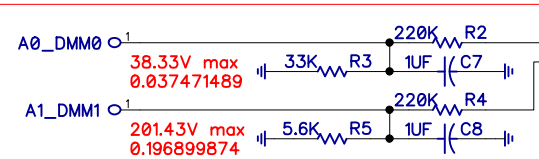
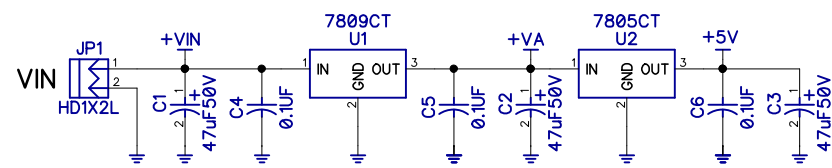
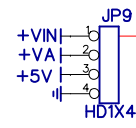
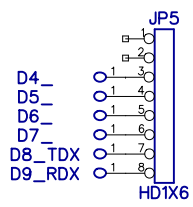
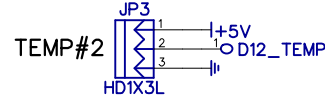
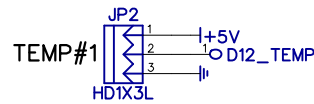
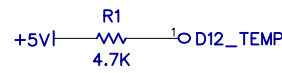
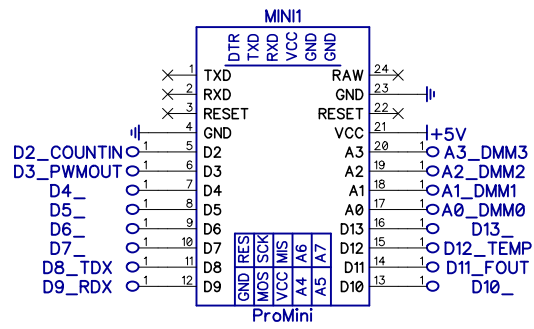
A

D

C

B

A



ISOLATED
CONSOLE INTERFACE

DMM0	DMM1	DMM2	DMM3	TEMP	COUNTER	PWM	UART
38V	200V	30A	x	YES	x	x	YES

PROPRIETARY NOTICE

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C. E. S. WEST COVINA, CALIFORNIA, U.S.A.

CPU SCHEMATICS

SIZE	DWG NO.	REV
A	CES180323-1	01
CES180323-1_v01.dch		Sheet 1 of x