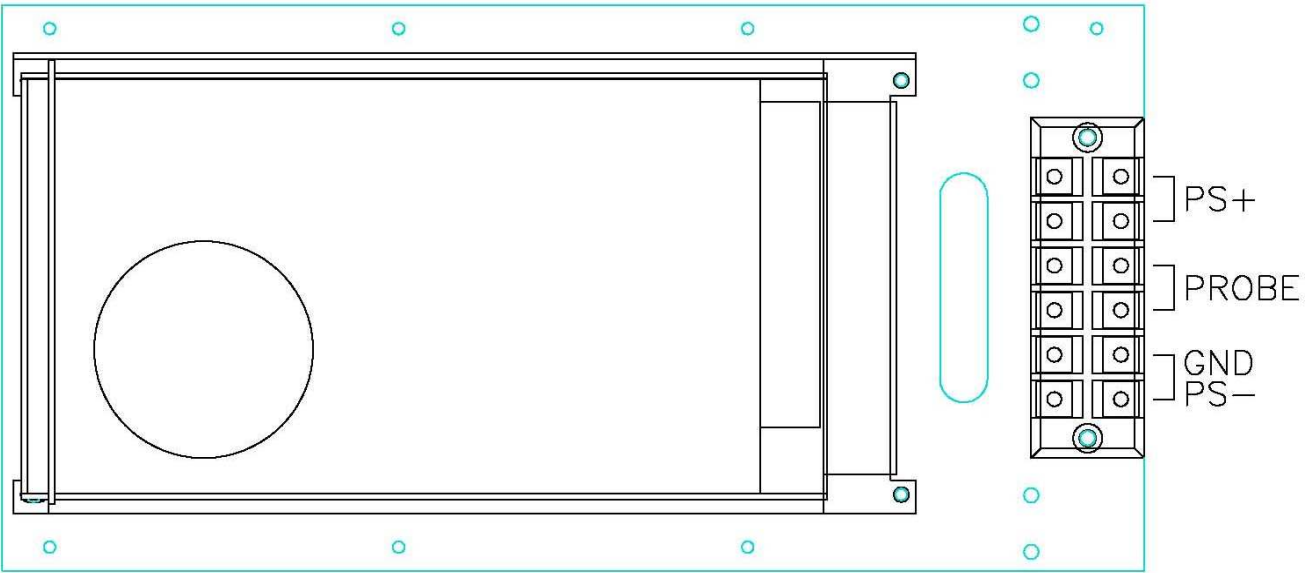
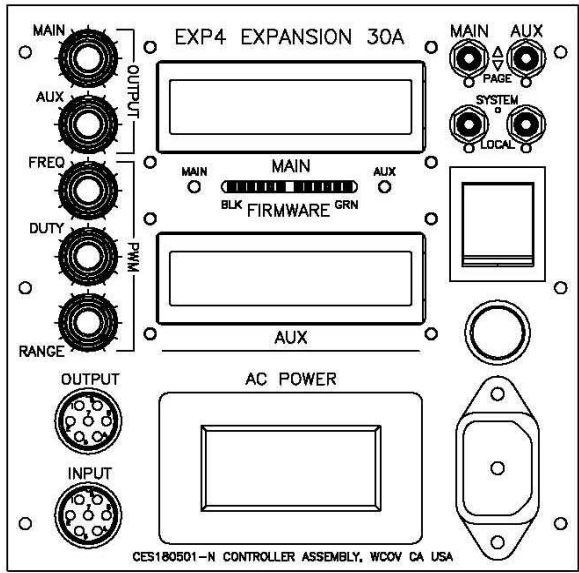
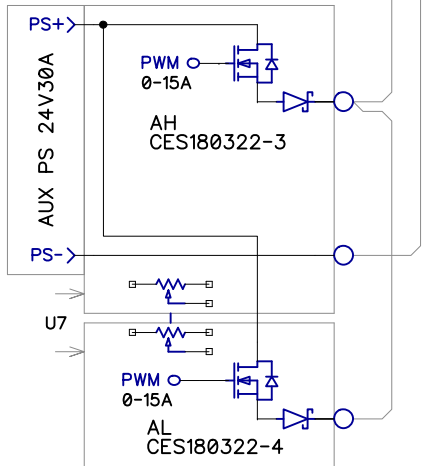
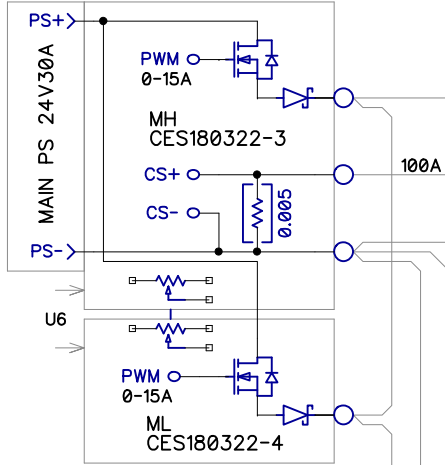




PROPRIETARY NOTICE THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION EXCLUSIVE TO C.E.S., AND SHALL NOT BE REPRODUCED, USED, OR DISCLOSED IN WHOLE OR IN PART TO OTHERS EXCEPT AS AUTHORIZED IN WRITING BY C.E.S..		C. E. S. WEST COVINA, CALIFORNIA, U.S.A.	
		EXPANSION SYSTEM BLOCK	
		SIZE DWG NO.	REV
A		EXP4	01a
EXP4B6_v01a_SystemBlock.dcn Sheet 1 of 5			

EMP4



CASCADE-OUT

GX16-9
pin 1 +15V(FL)
pin 2 0V(FL)
pin 3 GATE(HI)
pin 4 +15VCC
pin 5 GATE(LO)
pin 6 SYS-GND
pin 7 RXD
pin 8 TXD
pin 9 EDM_ON-

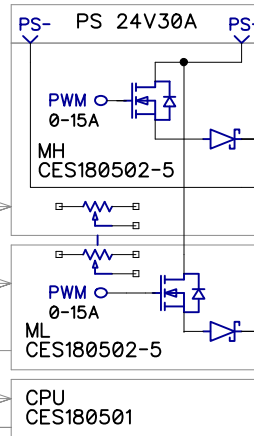
EXPANSION-OUT

GX16-7
pin 1 +15VCC
pin 2 GATE(LO)
pin 3 SYS-GND(RX-)
pin 4 RXD/RX+
pin 5 TXD/TX-
pin 6 +5VCC(TX+)
pin 7 EDM_ON-

CASCADE
GX16
9

EXPANSION
GX16
7

ECP4



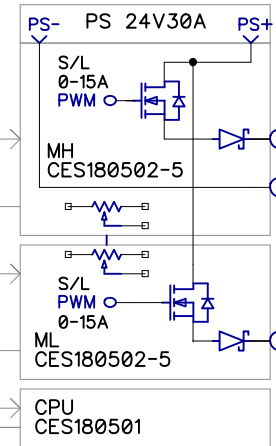
CASCADE-IN

GX16-9
pin 1 +15V(FL)
pin 2 0V(FL)
pin 3 GATE(HI)
pin 4 +15VCC
pin 5 GATE(LO)
pin 6 SYS-GND
pin 7 RXD
pin 8 TXD
pin 9 EDM_ON-

CASCADE-OUT

GX16-9
pin 1 +15V(FL)
pin 2 0V(FL)
pin 3 GATE(HI)
pin 4 +15VCC
pin 5 GATE(LO)
pin 6 SYS-GND
pin 7 RXD
pin 8 TXD
pin 9 EDM_ON-

EXP4



EXPANSION-IN

GX16-7
pin 1 +15VCC
pin 2 GATE(LO)
pin 3 SYS-GND(RX-)
pin 4 RXD/RX+
pin 5 TXD/TX-
pin 6 +5VCC(TX+)
pin 7 EDM_ON-

EXPANSION-OUT

GX16-7
pin 1 +15VCC
pin 2 GATE(LO)
pin 3 SYS-GND(RX-)
pin 4 RXD/RX+
pin 5 TXD/TX-
pin 6 +5VCC(TX+)
pin 7 EDM_ON-

EDMP4 SYSTEM CONFIG

PROPRIETARY NOTICE

THIS DOCUMENT CONTAINS
PROPRIETARY INFORMATION
EXCLUSIVE TO C.E.S., AND SHALL
NOT BE REPRODUCED, USED, OR
DISCLOSED IN WHOLE OR IN PART
TO OTHERS EXCEPT AS
AUTHORIZED IN WRITING BY C.E.S.

C. E. S. WEST COVINA, CALIFORNIA, U.S.A.

EXPANSION SYSTEM BLOCK

SIZE	DWG NO.	REV
A	EXP4B6_v01a_SystemBlock.doc	01a

EXP4B6_v01a_SystemBlock.doc Sheet 2 of 5

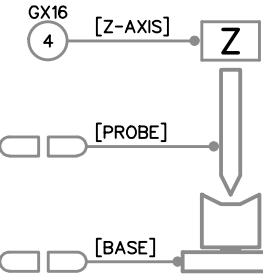
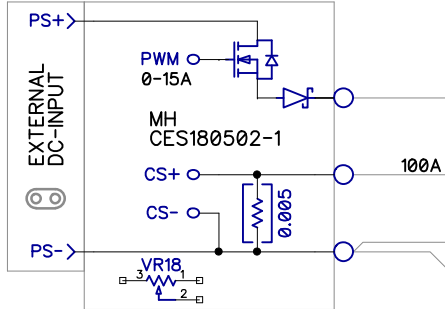
4

3

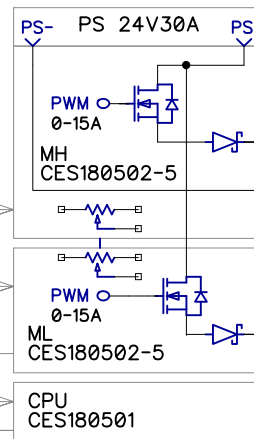
2

1

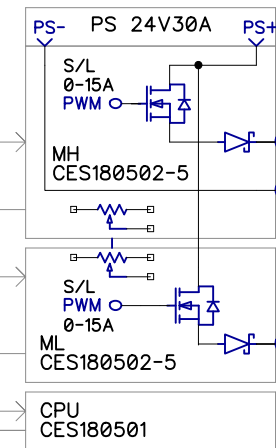
EMC4



ECP4



EXP4



CASCADE-OUT

GX16-9
 pin 1 +15V(FL)
 pin 2 0V(FL)
 pin 3 GATE(HI)
 pin 4 +15VCC
 pin 5 GATE(LO)
 pin 6 SYS-GND
 pin 7 RXD
 pin 8 TXD
 pin 9 EDM_ON-

CAS-OUT1
 GX16
 9

CASCADE-IN

GX16-9
 pin 1 +15V(FL)
 pin 2 0V(FL)
 pin 3 GATE(HI)
 pin 4 +15VCC
 pin 5 GATE(LO)
 pin 6 SYS-GND
 pin 7 RXD
 pin 8 TXD
 pin 9 EDM_ON-

CAS-OUT2
 GX16
 9

EXPANSION-OUT

GX16-7
 pin 1 +15VCC
 pin 2 GATE(LO)
 pin 3 SYS-GND(RX-)
 pin 4 RXD/RX+
 pin 5 TXD/TX-
 pin 6 +5VCC(TX+)
 pin 7 EDM_ON-

EXP-OUT1
 GX16
 7

EXP-OUT2
 GX16
 7

CASCADE-OUT

GX16-9
 pin 1 +15V(FL)
 pin 2 0V(FL)
 pin 3 GATE(HI)
 pin 4 +15VCC
 pin 5 GATE(LO)
 pin 6 SYS-GND
 pin 7 RXD
 pin 8 TXD
 pin 9 EDM_ON-

EXPANSION-IN

GX16-7
 pin 1 +15VCC
 pin 2 GATE(LO)
 pin 3 SYS-GND(RX-)
 pin 4 RXD/RX+
 pin 5 TXD/TX-
 pin 6 +5VCC(TX+)
 pin 7 EDM_ON-

EXPANSION-OUT

GX16-7
 pin 1 +15VCC
 pin 2 GATE(LO)
 pin 3 SYS-GND(RX-)
 pin 4 RXD/RX+
 pin 5 TXD/TX-
 pin 6 +5VCC(TX+)
 pin 7 EDM_ON-

EDMC4 SYSTEM CONFIG

PROPRIETARY NOTICE

THIS DOCUMENT CONTAINS
 PROPRIETARY INFORMATION
 EXCLUSIVE TO C.E.S., AND SHALL
 NOT BE REPRODUCED, USED, OR
 DISCLOSED IN WHOLE OR IN PART
 TO OTHERS EXCEPT AS
 AUTHORIZED IN WRITING BY C.E.S.

C. E. S. WEST COVINA, CALIFORNIA, U.S.A.

EXPANSION SYSTEM BLOCK

SIZE	DWG NO.	REV
A	EXP4	01a

EXP4B6_v01a_SystemBlock.doc Sheet 3 of 5

4

3

2

1

4

3

2

1

D

D

C

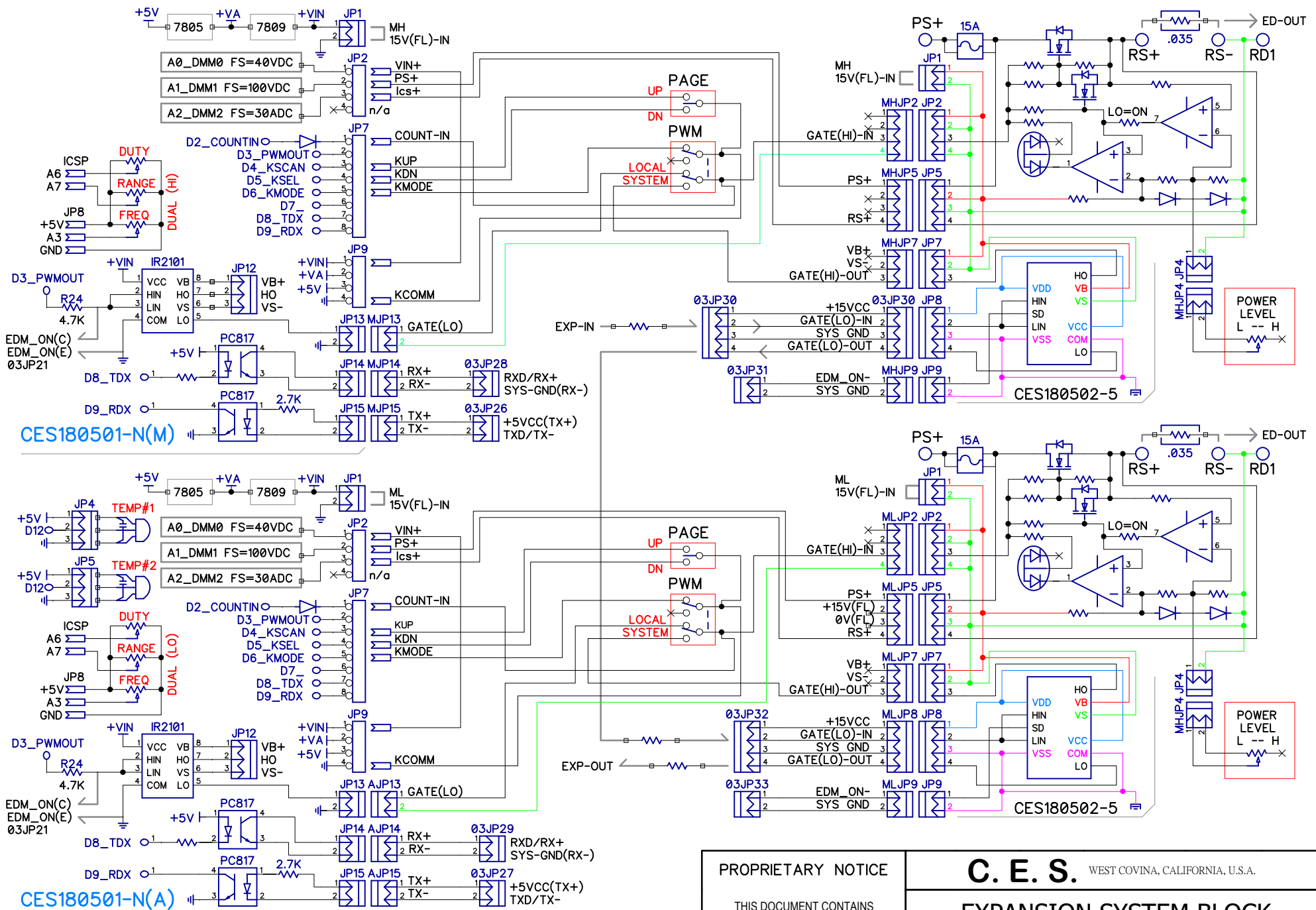
C

B

B

A

A



Note: Remove D6 on all 0502 assy.

- NOTE:
1. CES180502-5 POWER BOARD
 2. CES180501-N CPU BOARD
 3. CES180503-2 I/O BOARD
 4. ASYNC AUX LOCAL-PWM
 5. DCOMM PARALLEL T/R

PROPRIETARY NOTICE		C. E. S. WEST COVINA, CALIFORNIA, U.S.A.	
THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION EXCLUSIVE TO C.E.S., AND SHALL NOT BE REPRODUCED, USED, OR DISCLOSED IN WHOLE OR IN PART TO OTHERS EXCEPT AS AUTHORIZED IN WRITING BY C.E.S.		EXPANSION SYSTEM BLOCK	
SIZE	DWG NO.	REV	
A	EXP4	01a	
EXP4B6_v01a_SystemBlock.doc Sheet 4 of 5			

4

3

2

1

D

D

C

C

B

B

A

A

4

3

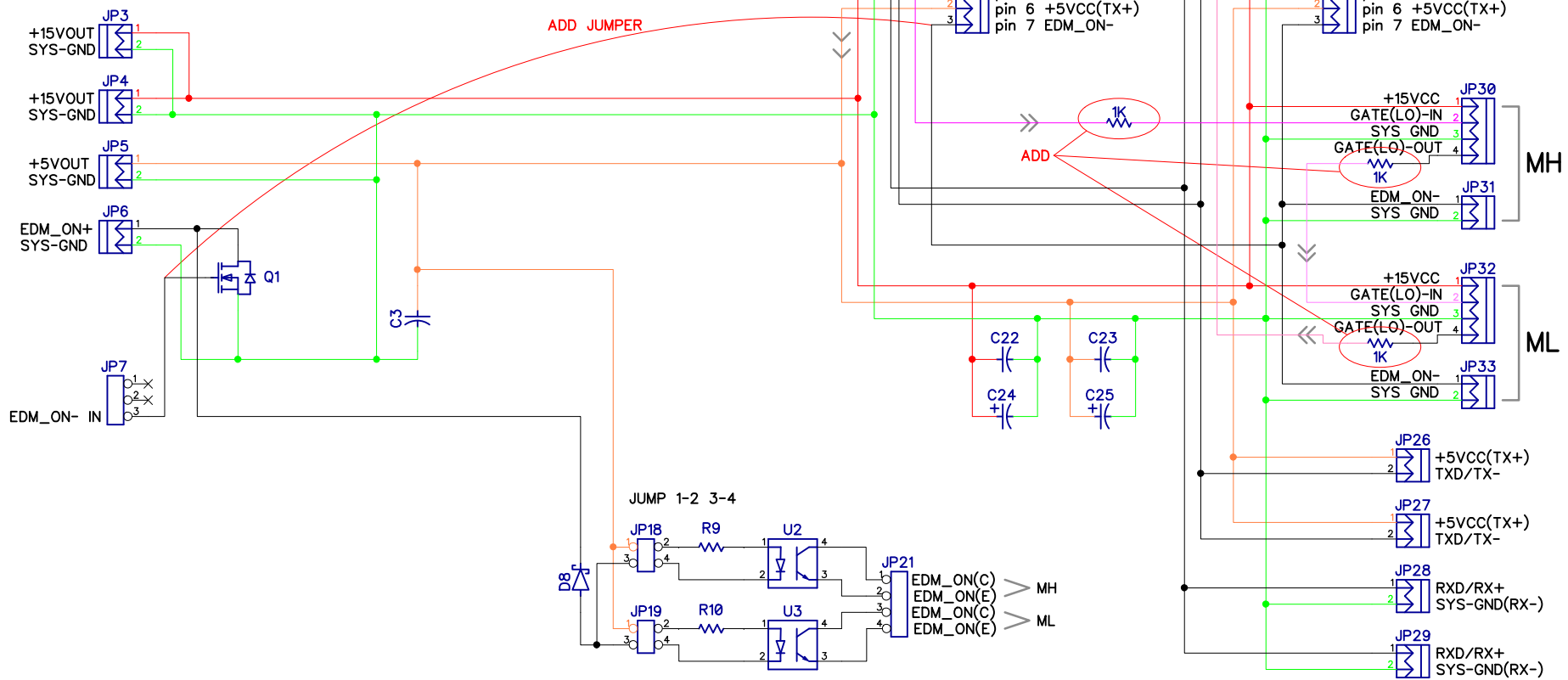
2

1

EXPANSION-I/O
GX16-7
pin 1 +15VCC
pin 2 GATE(LO)
pin 3 SYS-GND(RX-)
pin 4 RXD/RX+
pin 5 TXD/TX-
pin 6 +5VCC(TX+)
pin 7 EDM_ON-

EXPANSION-IN
GX16-7
pin 1 +15VCC
pin 2 GATE(LO) IN
pin 3 SYS-GND(RX-)
pin 4 RXD/RX+
pin 5 TXD/TX-
pin 6 +5VCC(TX+)
pin 7 EDM_ON-

EXPANSION-OUT
GX16-7
pin 1 +15VCC
pin 2 GATE(LO) OUT
pin 3 SYS-GND(RX-)
pin 4 RXD/RX+
pin 5 TXD/TX-
pin 6 +5VCC(TX+)
pin 7 EDM_ON-



CES180503-2 BLOCK

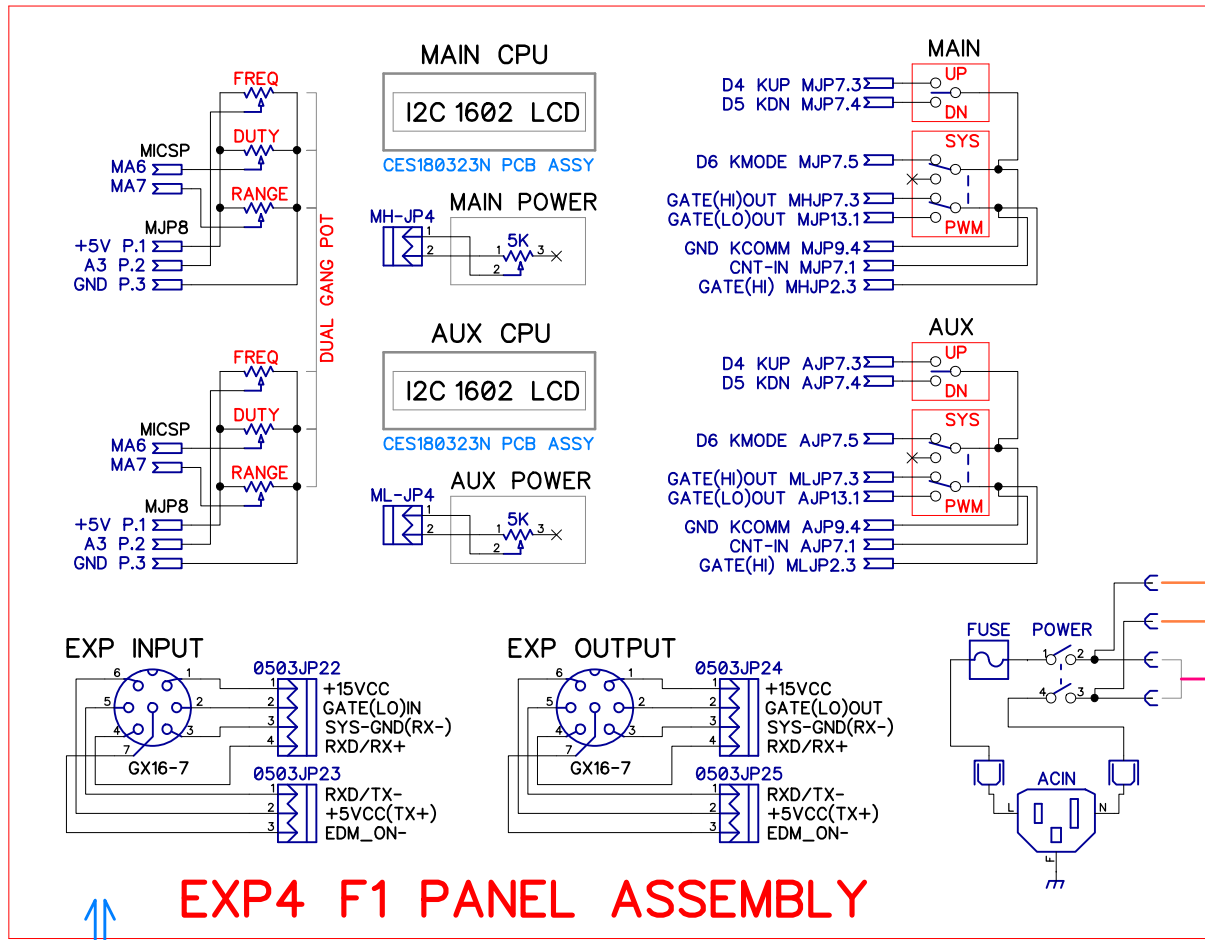
PROPRIETARY NOTICE

THIS DOCUMENT CONTAINS
PROPRIETARY INFORMATION
EXCLUSIVE TO C.E.S., AND SHALL
NOT BE REPRODUCED, USED, OR
DISCLOSED IN WHOLE OR IN PART
TO OTHERS EXCEPT AS
AUTHORIZED IN WRITING BY C.E.S..

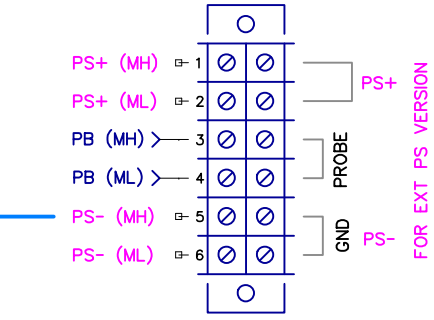
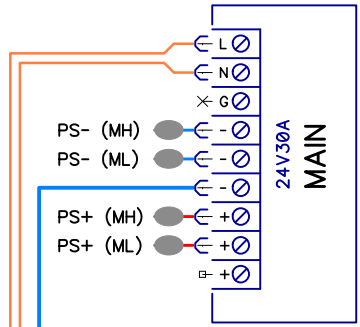
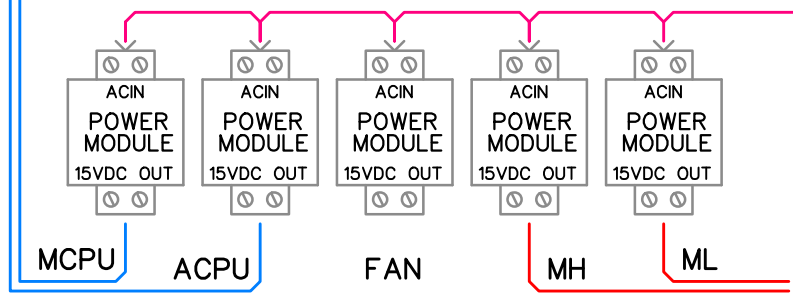
C. E. S. WEST COVINA, CALIFORNIA, U.S.A.

EXPANSION SYSTEM BLOCK

SIZE	DWG NO.	REV
A	EXP4	01a
EXP4B6_v01a_SystemBlock.dcn Sheet 5 of 5		



EXP4 F1 PANEL ASSEMBLY



PROPRIETARY NOTICE THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION EXCLUSIVE TO C.E.S., AND SHALL NOT BE REPRODUCED, USED, OR DISCLOSED IN WHOLE OR IN PART TO OTHERS EXCEPT AS AUTHORIZED IN WRITING BY C.E.S..	C. E. S. WEST COVINA, CALIFORNIA, U.S.A.		
	EXPANSION SYSTEM BLOCK		
	SIZE	DWG NO.	REV
A	EXP4	01a	
EXP4B6_v01a_SystemBlock.dcn			Sheet 6 of 5