



Sheet 1 of x

see Sheet 4
+24VDC IN
Share M2 Power

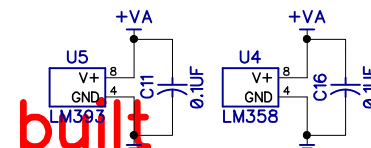
same as JP20

SAME AS JP25
see SHT 6

0V to +5V
Ext. POT
ISENSE Threshold
@Rcs 0.01 ohm
5A <> 0.5A

Reserved Option
FOR A3 as DMA Channel
NOT USED

EDMP3 v01 as built
CES171107 CONFIG CKT



PROPRIETARY NOTICE

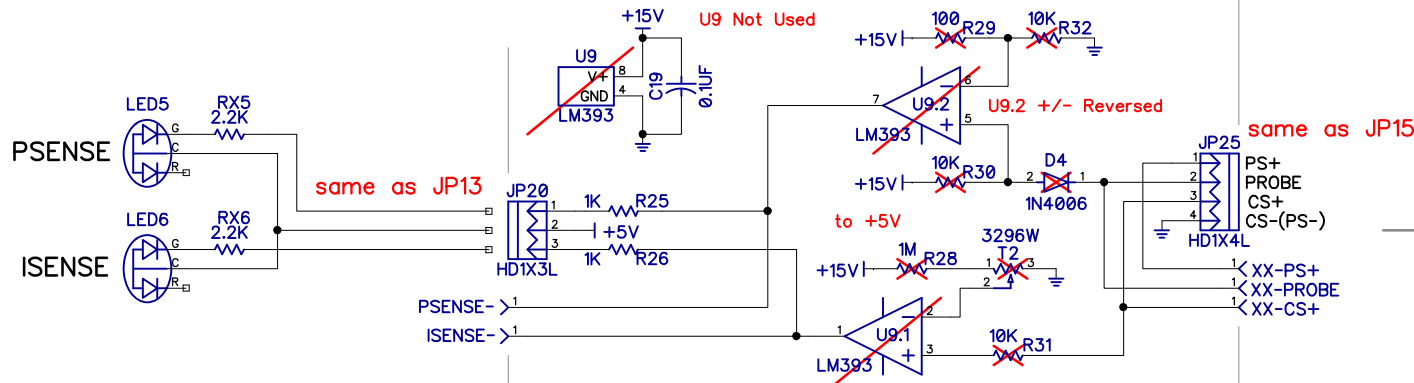
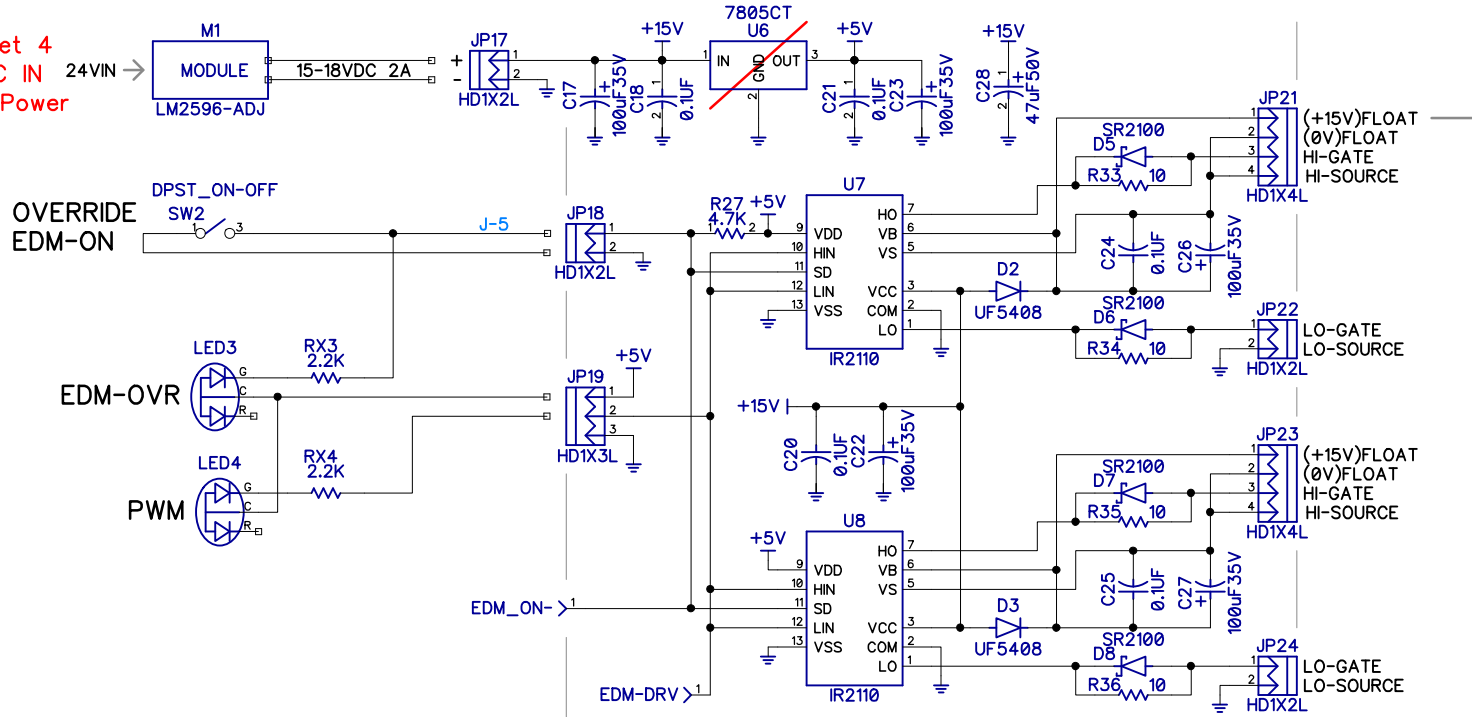
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EDMP3 SCHEMATICS

SIZE	DWG NO.	REV
A	EDMP3	01c
	EDMP3_v01c.dch	Sheet 2 of x

see Sheet 4
+24VDC IN
Share M2 Power



see Sheet 6,7,8

EDMP3 v01 as built
CES171107 CONFIG CKT

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EDMP3 SCHEMATICS

SIZE	DWG NO.	REV
A	EDMP3	01c
EDMP3_v01c.dch		Sheet 3 of x

4

3

2

1

D

D

C

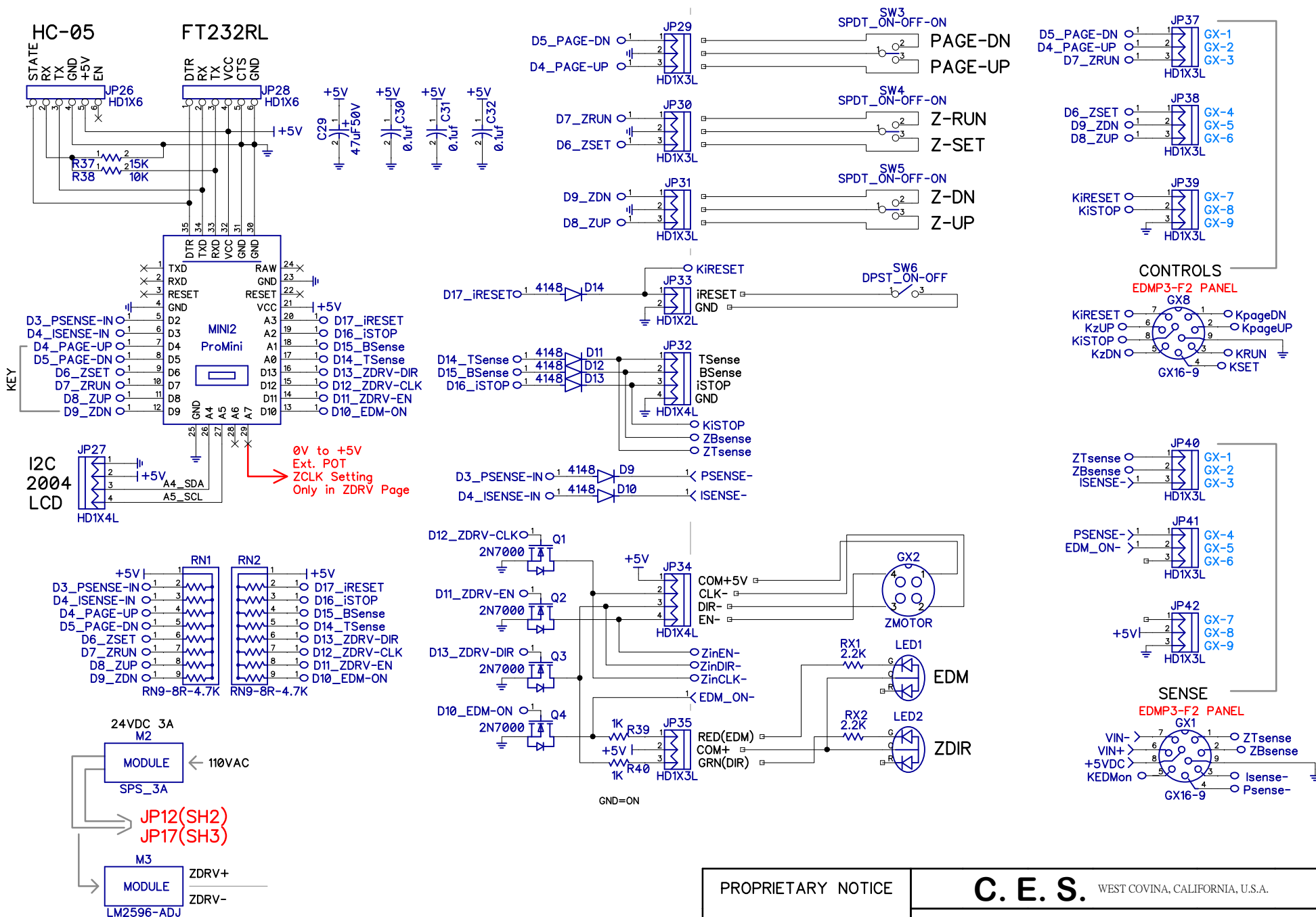
C

B

B

A

A

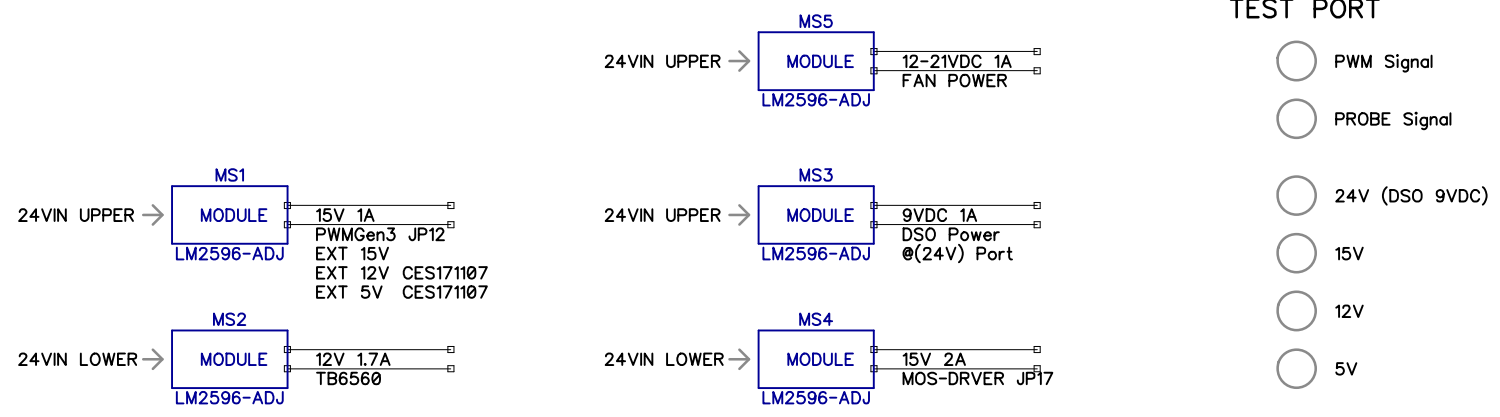


EDMP3 v01 as built
CES171107 CONFIG CKT

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		SIZE	DWG NO.		REV	
		A	EDMP3		01c	
		EDMP3_v01c.dch		Sheet 4 of x		

4 3 2 1

D



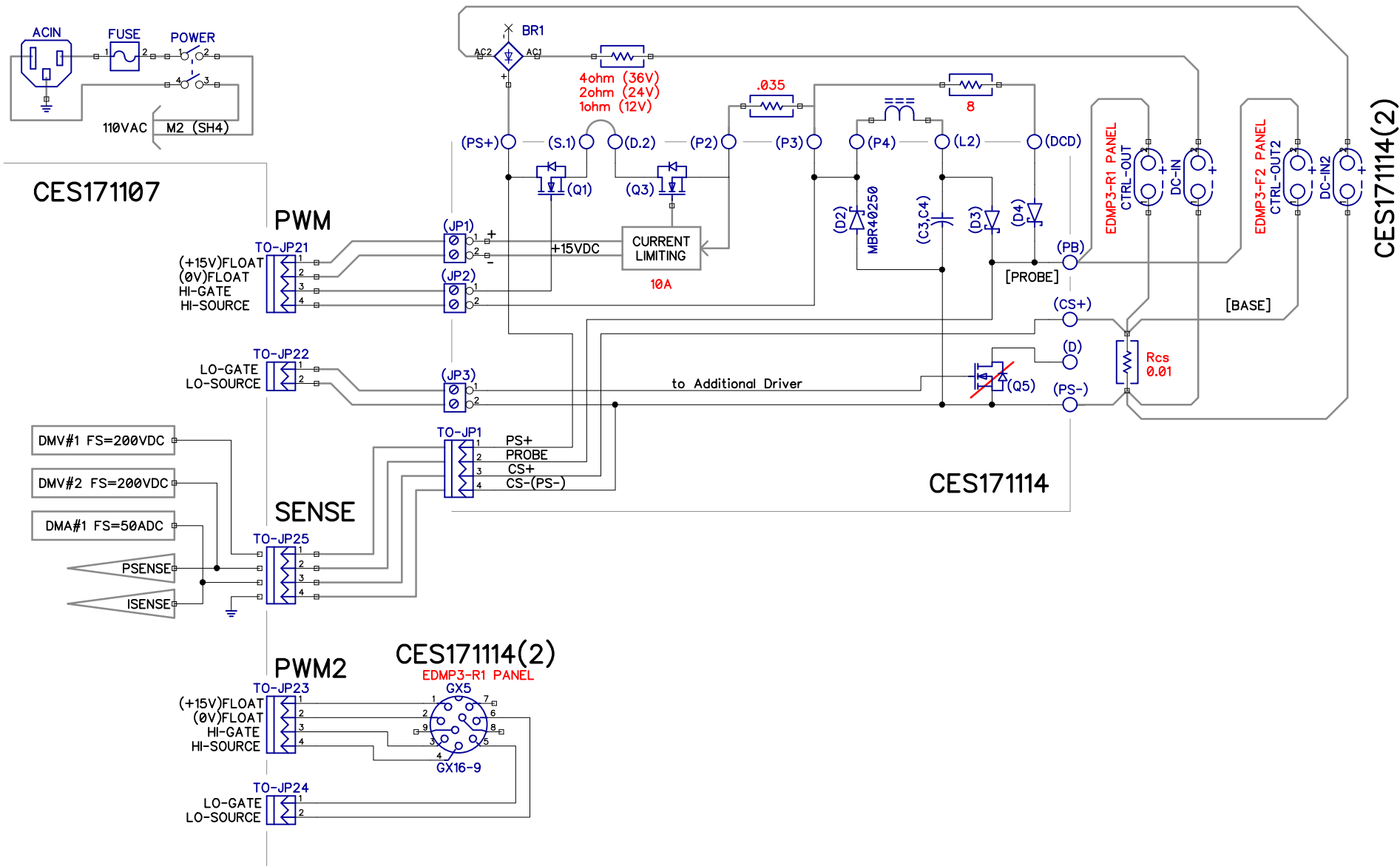
LEFT PANEL

A

EDMP3 v01 as built
CES171107 CONFIG CKT

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		EDMP3 SCHEMATICS	
		SIZE A	DWG NO. EDMP3 EDMP3_v01c.dch
		REV 01c	Sheet 5 of x

4 3 2 1



EDMP3 v01 as built
CES171107 CONFIG CKT

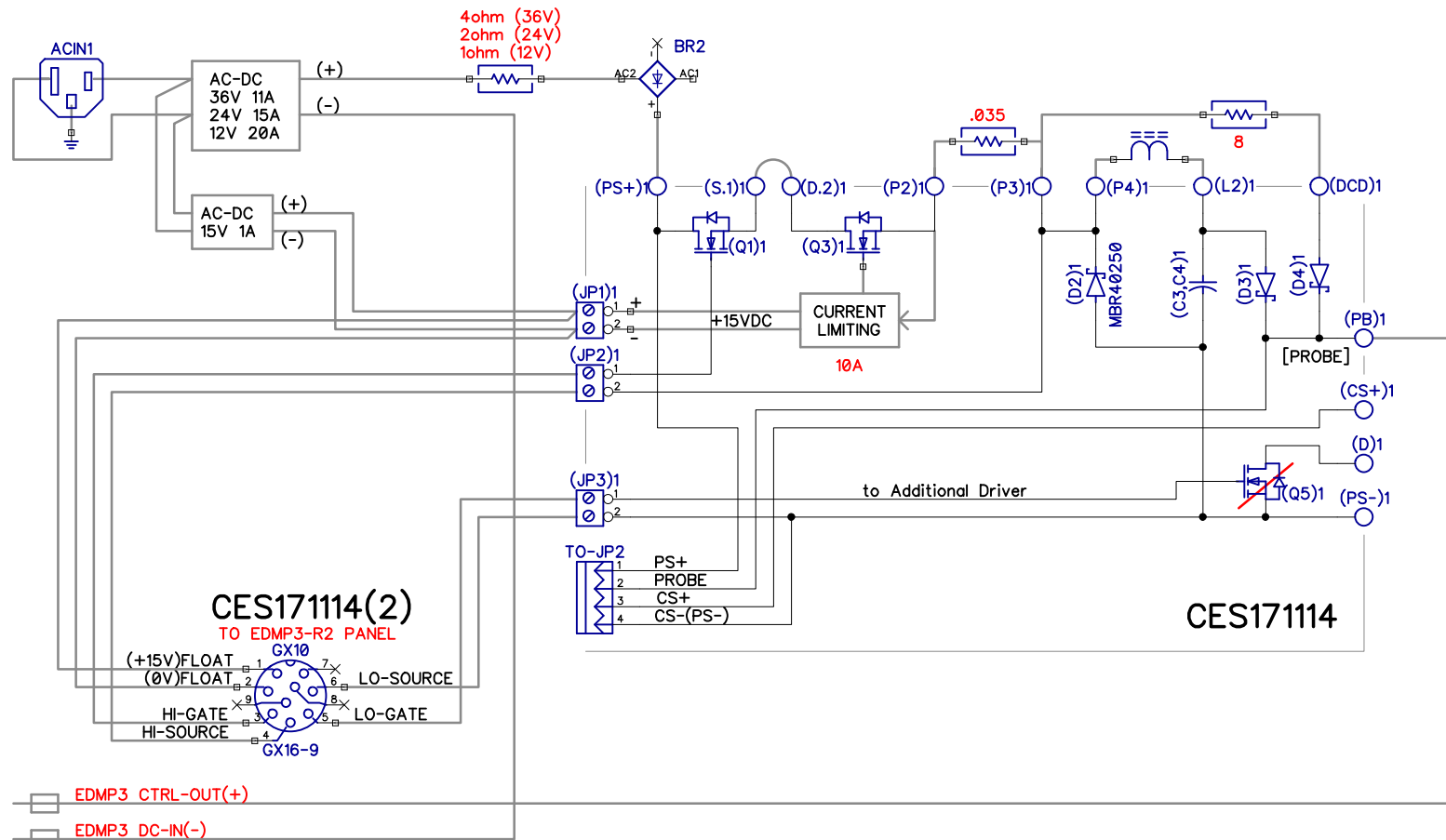
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EDMP3 SCHEMATICS

SIZE	DWG NO.	REV
A	EDMP3	01c
	EDMP3_v01c.dch	Sheet 6 of x



CES171114 EXTERNAL ADDIN CONFIG CKT

EDMP3 v01 as built
CES171107 CONFIG CKT

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EDMP3 SCHEMATICS

SIZE	DWG NO.	REV
A	EDMP3	01c
EDMP3_v01c.dch		Sheet 7 of x

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EDMP3 SCHEMATICS

SIZE	DWG NO.	REV
A	EDMP3	01c
EDMP3_v01c.dch		Sheet 8 of x

